# Mx7x <br> DNP3 for Bitronics 70 Series 

## Manual

Mx7x

## DNP3 for Bitronics 70 Series

Publication Reference: Mx7xD/EN/M/E

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## 70 SERIES Firmware versions

The following table provides the most recent firmware and software versions. For best results, the Configurator version used should match with the firmware version. A complete list of firmware and software versions is provided on the 70 Series Utilities CD.

| Firmware Versions |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Description | Bios Version | DSP <br> Firmware | Host Firmware | Configu rator | Utilities CD | Release Date |
| M870 Family |  |  |  |  |  |  |
| Mx7x Product Release, New Hardware supported Dual Bus, Analog I/O | 2.1/3.0* | 1.210 | 2.050 | 2.31 | 2.43 | 03/24/06 |
| Mx7x Updated Release | 2.1/3.0* | " | 2.060 | 2.32 | 2.44 | 04/14/06 |
| Mx7x Updated Release | 2.1/3.0* | 1.240 | 2.120 | 2.39 | 2.50 | 10/01/06 |
| M87x Updated Release | 2.1/3.0* | 1.240 | 2.150 | 2.41 | 2.52 | 12/18/06 |
| M87x Product Release, Fault Location, Adjustable Sample Rate | 3.4 | 1.30 | 2.170 | 2.43 | 2.56 | 12/21/07 |
| M87x Product Release; Add Demand per phase for Watts, VAr, \& VA. Configurator \& Biview improvements w/ modems. Change to Digital I/O default watchdog contact (Configurator setup; not firmware dependent). Support new version of hardware on P3x, P4x modules. | 3.40 | 1.30 | 2.18 | 3.00A | 2.57 | 10/17/08 |
| M87x Product Release: Added 1 mHz accuracy on M87x. Improved poll rate from 500ms to 100 ms for a single P40 transducer inputs module (M87x). Fault distance configuration is changed. Time sync with respect to DNP master is changed from the DNP master jamming the time to asking the master what time to jam. Increased waveform recording limit from 999 post trigger for longer recording. | 3.40 | 1.31 | 2.19 | 3.02 | 2.58 | 09/30/09 |
| M87x Product Release, IEC61850 \& SNTP; Avg 3-Ph Amps and Avg 3-Ph Volts | 3.40 | 1.30 | 3.01 .0 | 3.01 | 3.01 | 1/30/09 |

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## 70 SERIES MANUAL SET

M87x User Manual
M57x User Manual
70 SERIES Modbus Protocol
70 SERIES DNP3 Protocol
M870D Remote Display Manual
M570Dx Remote Display Manual
70 SERIES IEC $61850{ }^{\circledR}$ Protocol Manual

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Please refer to the M87x and M57x User Manuals for information regarding safety, installation, commissioning and decommissioning.

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## DNP INTERFACE

### 1.1 Description

The DNP network is a "MASTER" to "SLAVE" network; that is to say, one node asks a question and a second node answers. A NODE is a DNP device (RTU, Computer, M871, etc.) that is connected to the network. Each DNP NODE has an ADDRESS in the range of 0 to 65535, and it is this address that allows a MASTER to selectively request data from any other device. DNP uses the address 65535 for broadcast functions. Broadcast requests NEVER generate DNP responses.

The DNP implementation in the 70 SERIES IEDs conforms to all of the Harris IED (Intelligent Electronics Devices) implementation guidelines. All data items that are available from the 70 SERIES IEDs can be obtained via the DNP READ CLASS 0 command. Individual items can also be read using READ BINARY OUTPUT STATUS or READ ANALOG INPUT or READ COUNTER or READ BINARY INPUT STATUS or READ ANALOG OUTPUT STATUS commands.

The Energy values can be reset to zero by issuing the DIRECT OPERATE, DIRECT OPERATE NO ACKNOWLEDGE, or SELECT BEFORE OPERATE by using the CONTROL RELAY OUTPUT BLOCK object.

The Demand values can be reset by issuing the same DIRECT OPERATE, DIRECT OPERATE NO ACKNOWLEDGE, or SELECT BEFORE OPERATE command to the other points of this object. Waveform and Disturbance Records can be triggered and digital outputs on the optional Digital I/O Module can be activated and cleared using these commands.

The CT and PT ratios, Current and Voltage Scale Factors, and various other configuration parameters can be changed by issuing DIRECT OPERATE, DIRECT OPERATE NO ACKNOWLEDGE, or SELECT BEFORE OPERATE by using the ANALOG OUTPUT BLOCK object. Due to the limited number of FLASH write cycles, scale factors SHOULD NOT be written continuously. Refer to Section 1.5 for more information on setting CT and PT ratios and Current and Voltage Scale Factors.

The SELECT BEFORE OPERATE arm timeout value is configurable from zero seconds to twenty-four hours.

### 1.2 DNP Address

Each DNP instrument responds to a single destination address in the range 0-65519. Each instrument on a DNP link must have a unique address. The 70 SERIES IEDs will allow any of the 65526 addresses to be selected. DNP instruments also use a GLOBAL address of 65535. Requests sent to the GLOBAL address cause the instrument to execute the function but not to respond.

### 1.3 Transaction Timing

The 70 SERIES IEDs complete a set of calculations approximately every cycle and calculations for volt and amp measurements every quarter cycle. The HOST CPU processor services the DNP ports by interrupts received from the corresponding serial ports. Incoming messages are parsed and responded to in approximately 30 ms .

### 1.4 Object Format

The 70 SERIES IEDs report all static measurements via the use of three static objects. These objects include COUNTER (object 20, variations 1, 2, 5 and 6), ANALOG INPUT (object 30, variations 1, 2, 3 and 4) and BINARY INPUT (object 01, variations 1 and 2). These objects are read only and cannot be modified by DNP MASTER devices.

Parameters, such as configuration registers, that can be modified make use of the ANALOG OUTPUT STATUS (object 40, variation 2) object. DNP MASTER devices can read these points or modify their value via the ANALOG OUTPUT BLOCK (object 41, variation 2) command. Digital Outputs and all other pseudo output points (such as demand and energy resets) are reported using the BINARY OUTPUT STATUS (object 10, variation 2). BINARY OUTPUTs can be PULSED ON, PULSED OFF or LATCHED ON and LATCHED OFF by using the CONTROL RELAY OUTPUT BLOCK (object 12, variation 1) command.

The 70 SERIES IEDs are capable of reporting BINARY INPUT CHANGE (object 02, variations 1 and 2) events with and without time and ANALOG CHANGE EVENTS (object 32, variations 2 and 4) with and without time. Any BINARY INPUT can be configured to report as a CLASS-1, CLASS-2, or CLASS-3 BINARY INPUT CHANGE event and any ANALOG INPUT can be configured to report as a CLASS-1, CLASS-2, or CLASS-3 ANALOG CHANGE EVENT. Point, Class, Analog Deadband Values and Object Variation are all selectable by use of the 70 SERIES configuration utility software. The Object Variations are selectable by Object (not by point). All ANALOG CHANGE EVENTS can be configured to report with or without time, and all BINARY INPUT CHANGE objects can be configured to report with or without time.

The DNP protocol allows each device to determine the best method of data transfer. The 70 SERIES IEDs support this by selecting the most appropriate response variation when either the requested variation is 0 or a CLASS-0 read is requested. Both COUNTER and ANALOG INPUT objects allow optional flags to be used. If a value is requested as variation 0 , the 70 SERIES IEDs respond as if the requested variation was for a 32-bit COUNTER or 16-bit ANALOG INPUT or 16-bit ANALOG OUTPUT STATUS.

When reading objects, the Health Check point (object 30, point 0 ) should always be read and checked before interpreting data, since some failure modes will cause erroneous data to be presented (See Section 1.8). The majority of the points are represented in Normalized 2's complement format. For conversion of the point data into engineering units, please refer to Section 1.6. For specifics concerning the correct command and its implementation, users are directed to the M87X or M57X User Manuals for the specific device that will request the data. Listed in section 1.4.1 are the point assignments for the 70 SERIES IEDs when using the Configurable or Single Feeder Configurable (SFC) selection (used for M571 and M871). NOTE: The 70 Series IEDs have a total of up to 6 different point sets (depending upon Configurator and fimrware versions). Please refer to the 70 Series Configurator DNP Points tab and click on the various options in the "Point Set" and "Points to Display" sections to see point assignments. For Configurator versions 2.27 or higher, the Point Sets on the left side of the box in the DNP Points Screen are all fixed sets whereas the ones on the right side of the box are all configurable to varying degrees (some have a fixed portion followed by a section where the user can select any of the measurements available in the 70 Series IEDs) Note also that unless otherwise specified, all points are READ ONLY.
1.4.1 70 SERIES IEDs DNP3 Point Assignments (Configurable/SFC (Single Feeder Configurable))


## 70 SERIES IEDs DNP3 Point Assignments

|  | DNP Point | Contents | Data | Scale | Ind | Values/Dependencies | Type | Min | Max | Step | Pass |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | Bit-7 | Reserved |  |  |  |  |  |
|  |  | Bit-8 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-9 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-10 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-11 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-12 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-13 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-14 |  |  | Reserved |  |  |  |  |  |
|  |  | Bit-15 |  |  | Reserved |  |  |  |  |  |
| Al:02 |  |  | Amps A | T2 | Amp <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *10*Amp Scale) A |  |
| Al:03 |  |  | Amps B | T2 | Amp <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *10*Amp Scale) A |  |
| Al:04 |  |  | Amps C | T2 | Amp <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *10*Amp Scale) A |  |
| Al:05 |  |  | Amps N | T3 | Amp Scale |  |  | Data | 0 | 32767 | ((1/32768) *15*Amp Scale) A |  |
| Al:06 |  |  | Amps Residual | T3 | Amp <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *15*Amp Scale) A |  |
| Al:07 |  |  | Volts A | T4 | Volt Scale |  |  | Data | 0 | 32767 | ((1/32768) *150*Volt Scale) V |  |
| Al:08 |  |  | Volts B | T4 | Volt Scale |  |  | Data | 0 | 32767 | ((1/32768) *150*Volt Scale) V |  |
| Al:09 |  |  | Volts C | T4 | Volt Scale |  |  | Data | 0 | 32767 | ((1/32768) *150*Volt Scale) V |  |
| Al:10 |  | Volts N | T4 | Volt Scale |  |  | Data | 0 | 32767 | ((1/32768) *150*Volt Scale) V |  |
| Al:11 |  | Volts AB | T4 | Volt Scale |  |  | Data | 0 | 32767 | ((1/32768) *150*Volt Scale) V |  |

70 SERIES IEDs DNP3 Point Assignments


70 SERIES IEDs DNP3 Point Assignments

| DNP P | Contents | Data | Scale | Ind | Values/Dependencies | Type | Min | Max | Step | Pass |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Al:25 | VARs A | T5 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | -32768 | 32767 | ((1/32768) *1500*Amp Scale * Volt Scale) vars |  |
| Al:26 | VARs B | T5 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | -32768 | 32767 | ((1/32768) *1500*Amp Scale * Volt Scale) vars |  |
| Al:27 | VARs C | T5 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | -32768 | 32767 | ((1/32768) *1500*Amp Scale * Volt Scale) vars |  |
| Al:28 | VARs Total | T6 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | -32768 | 32767 | ((1/32768) *4500*Amp Scale * Volt Scale) vars |  |
| Al:29 | VAs A | T5 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *1500*Amp Scale * Volt Scale) VAs |  |
| Al:30 | VAs B | T5 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *1500*Amp Scale * Volt Scale) VAs |  |
| Al:31 | VAs C | T5 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *1500*Amp Scale * Volt Scale) VAs |  |
| Al:32 | VAs Total Geometric | T6 | Amp <br> Scale * <br> Volt <br> Scale |  |  | Data | 0 | 32767 | ((1/32768) *4500*Amp Scale * Volt Scale) VAs |  |
| Al:33 | Power Factor A | T7 |  |  |  | Data | -1000 | 1000 | 0.001 |  |
| Al:34 | Power Factor B | T7 |  |  |  | Data | -1000 | 1000 | 0.001 |  |
| Al:35 | Power Factor C | T7 |  |  |  | Data | -1000 | 1000 | 0.001 |  |
| Al:36 | Power Factor Total Geometric | T7 |  |  |  | Data | -1000 | 1000 | 0.001 |  |

70 SERIES IEDs DNP3 Point Assignments


70 SERIES IEDs DNP3 Point Assignments


70 SERIES IEDs DNP3 Point Assignments


## 70 SERIES IEDs DNP3 Point Assignments

| DNP P | Contents | Data | Scale | Ind | Values/Dependencies | Type | Min | Max | Step | Pass |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| AO:47 | User Phase Correction Volts C Bus2 | T8 |  |  |  | Setting | -18000 | 18000 | 0.01 Degrees |  |
| AO:48 | User Phase Correction Volts N Bus2 | T8 |  |  |  | Setting | -18000 | 18000 | 0.01 Degrees |  |
| AO:49 | User Phase Correction Amps A | T8 |  |  |  | Setting | -18000 | 18000 | 0.01 Degrees |  |
| AO:50 | User Phase Correction Amps B | T8 |  |  |  | Setting | -18000 | 18000 | 0.01 Degrees |  |
| AO:51 | User Phase Correction Amps C | T8 |  |  |  | Setting | -18000 | 18000 | 0.01 Degrees |  |
| AO:52 | User Phase Correction Amps N | T8 |  |  |  | Setting | -18000 | 18000 | 0.01 Degrees |  |

### 1.4.2 DNP3 Calculation-Type Codes

| Type | Value / Bit Mask | Description |
| :---: | :---: | :---: |
| T1 |  | Unsigned 16-Bit Integer |
| T2 |  | Signed 16-Bit Integer - 2's Complement - Saturation 10 |
|  |  | Float Value $=($ ( Integer Value) / 32768) * Scale * 10) |
|  |  | Example: 5.0 A stored as 16384 when Amp Scale $=1: 1$ |
| T3 |  | Signed 16-Bit Integer - 2's Complement - Saturation 15 |
|  |  | Float Value $=($ ( Integer Value) / 32768) * Scale * 15) |
|  |  | Example: 150 A stored as 16384 when Amp Scale $=20: 1$ |
| T4 |  | Signed 16-Bit Integer - 2's Complement - Saturation 150 |
|  |  | Float Value $=($ (Integer Value) $/ 32768) *$ Scale * 150) |
|  |  | Example: 119.998 V stored as 26214 when Volt Scale $=1: 1$ |
| T5 |  | Signed 16-Bit Integer - 2's Complement - Saturation 1500 |
|  |  | Float Value $=($ ( Integer Value) / 32768) * Scale * 1500) |
|  |  | Example: -750.0 W stored as -16384 when Volt Scale = 1:1, Amp Scale 1:1 |
| T6 |  | Signed 16-Bit Integer - 2's Complement - Saturation 4500 |
|  |  | Float Value $=($ (Integer Value) / 32768) * Scale * 4500) |
|  |  | Example: -90.0 kW stored as -8192 when Volt Scale $=20: 1$, Amp Scale 4:1 |
| T7 |  | Signed 16-Bit Integer - 2's Complement - 3 Decimal Places |
|  |  | Example: -12.345 stored as -12345 |
| T8 |  | Signed 16-Bit Integer - 2's Complement - 2 Decimal Places |
|  |  | Example: 123.45 stored as 12345 |
| T9 |  | Signed 16-Bit Integer-2's Complement -1 Decimal Place |
|  |  | Example: -1234.5 stored as -12345 |
| T10 |  | Unsigned 16-Bit Integer - Normalized Ratio |
|  |  | ratio $=$ (Normalized Ratio $/$ Ratio Divisor) |
|  |  | Example : 1.234, 12.34, 123.4, and 1234 are all stored as 1234 |
| T11 |  | Unsigned 16-Bit Integer - Ratio Divisor |
|  |  | ratio $=$ (Normalized Ratio / Ratio Divisor); valid Ratio Divisors are 1,10,100,1000 |
|  |  | Example: X. XXX stored as 1000, XX.XX stored as 100, XXX. ${ }^{\text {d }}$ stored as 10 |
| T12 |  | Signed 16-Bit - 2's Complement - Saturation 2 |
|  |  | Gain Value = Integer Value /16384) |
|  |  | Example: -0.250 stored as -4096 |
| T13 |  | Unsigned 16-Bit Integer - 12 Bit Offset Binary - Saturation 10 |
|  |  | Float Value $=($ (Integer Value - 2047) / (2048) ) * Scale * 10 |
|  |  | Example: 5.0 A stored as 3071 when Amp Scale 1:1 |
| T14 |  | Unsigned 16-Bit Integer - 12 Bit Offset Binary - Saturation 150 |
|  |  | Float Value $=($ (Integer Value - 2047) / (2048) ) * Scale * 150 |
|  |  | Example: 119.97 V stored as 3685 when Volt Scale 1:1 |
| T15 |  | Unsigned 16-Bit Integer-12 Bit Offset Binary - Saturation 1000 |
|  |  | Float Value $=\left(\begin{array}{l}\text { (Integer Value - 2047) } /(2048) ~) ~ * ~ S c a l e ~ * ~ \\ 1000\end{array}\right.$ |
|  |  | Example: -500 W stored as 1023 when Volt Scale $=1: 1$, Amp Scale $=1: 1$ |
| T16 |  | Unsigned 16-Bit Integer - 12 Bit Offset Binary - Saturation 3000 |
|  |  | Float Value $=($ (Integer Value - 2047) / (2048) ) * Scale * 3000 |
|  |  | Example: 349.10 kW stored as 3040 when Volt Scale $=6: 1$, Amp Scale $=40: 1$ |
| T17 |  | Unsigned 16-Bit Integer - 12 Bit Offset Binary - Saturation 15 |
|  |  | Float Value $=\left(\begin{array}{l}\text { (Integer Value - 2047) } /(2048) ~) ~ * ~ S c a l e ~ * ~ \\ 15\end{array}\right.$ |
|  |  | Example: 11.79 A stored as 2369 when Amp Scale 5:1 |
| T18 |  | Unsigned 16-Bit Integer - 12 Bit Offset Binary -1 Decimal Place |
|  |  | Float Value $=($ ( Integer Value - 2047) / (10) ) |
|  |  | Example: 121.4 degrees stored as 3261 |


| Type | Value / Bit Mask | Description |
| :---: | :---: | :---: |
| T19 |  | Unsigned 16-Bit Integer - 12 Bit Offset Binary -3 Decimal Place |
|  |  | Float Value $=(($ Integer Value - 2047) $/(1000)$ ) |
|  |  | Example: 0.978 Power Factor stored as 3025 |
| T20 |  | Unsigned 16-Bit Integer - Bit Control/Status |
|  |  | 0' - stored as zero; '1' - stored as 65536 |
| T21 |  | Unsigned 16-Bit Integer - 3 Decimal Places |
|  |  | Example: 54.321 stored as 54321 |
| T22 |  | Bit |
|  |  | Example: 1-bit is set, 0-bit is clear |

### 1.5 Configuration

### 1.5.1 Setting CT and VT Ratios

The 70 SERIES IEDs are capable of internally storing and recalling CT and VT ratios. The CT and VT ratios are written to ANALOG OUTPUTS over the DNP communication port, and are stored in non-volatile memory on the CT/VT Module. Each ratio is stored in two points, one for the Normalized Ratio and the other for the Ratio Divisor. Allowable constants for the normalized ratios are 1000 to 9999 . The Ratio Divisors may be 1, 10, 100, or 1000 only. The number stored will be the high side rating of the CT Ratio or VT Ratio. Both a 500:5 ratio CT and a 100:1 CT will have a value of 100 stored. For example, to calculate a CT and VT ratio for Phase A from the data stored in the 70 SERIES IEDs, use the following equation:

$$
\begin{aligned}
\text { Phase } A C T_{\text {RATIO }} & =\frac{\text { Phase } A C T \text { Value }(A O: 21)}{\text { Phase } A C T \text { RatioDiviso }(A O: 22)} \\
\text { Phase } A V T_{\text {RATIO }} & =\frac{\text { Phase } A V T \text { Value }(A O: 05)}{\text { Phase } A V T \text { RatioDivisor }(A O: 06)}
\end{aligned}
$$

The 70 SERIES IEDs calculate all measured quantities in primary units. The CT and VT ratio information is used to calculate these primary values. To force the 70 SERIES IEDs to report in secondary units, set the Scale Factor = to the CT or VT ratio, as appropriate.

NOTE: The Full Scale Integer Value of current and voltage reported by the 70 SERIES IEDs over DNP can be changed, see Section 1.5.2.

In the event of a CT/VT Ratio Checksum Failure, the value in the Normalized CT Ratio and Normalized VT Ratio points default to 1000, and the value in the CT Ratio Divisor and VT Ratio Divisor default to 1000. This results in a 1:1 CT Ratio and 1:1 VT Ratio.

## WARNING - TO PRESERVE SYSTEM PERFORMANCE, ONLY WRITE TO RATIO REGISTERS WHEN THE RATIOS NEED TO BE CHANGED.

### 1.5.2 Setting Current and Voltage Scale Factors

As detailed in Section 1.6, the data in the 70 SERIES IEDs DNP points is in Normalized 2's complement format. Measurements presented in this format do not have as much resolution as the 70 SERIES IED's internal floating-point values. Because of the wide dynamic range of the device inputs, the default full-scale integer representation of measurement values is a compromise that has been selected to accommodate typical system signal levels, while giving reasonable resolution. The maximum (or full scale) integer value that can be reported corresponds to some particular level of Amperes, Volts, Watts, etc.

The maximum full scale integer value of Amperes and Volts in the Normalized 2's complement format can be changed by means of the Current Scale Factor and Voltage Scale Factor ( $I_{\text {SCALE }}$ factor and $V_{\text {SCALE }}$ factor), which are modified by writing to the Normalized Scale Factor and Scale Factor Divisor (AO:01 to AO:04) points. These Current Scale Factor and Voltage Scale Factor values are multipliers of the Default Full Scale values. To convert values reported in DNP points to engineering units, refer to Section 1.6. The default full-scale values for quantities are:

| Quantity | Default <br> Full Scale |
| :--- | :--- |
| Phase Current | 10 |
| Neutral Current | 15 |
| Voltages | 150 |
| Per-Phase Power (Watt, VAR, VA) | 1500 |
| Total Power (Watt, VAR, VA) | 4500 |

$I_{\text {SCALE FACTOR }}=\frac{\text { Normalized Current ScaleFactr }(\text { AO:03 })}{\text { Current ScaleFadar Divisar (AO:04) }}$
$V_{\text {SCALE FACTOR }}=\frac{\text { NormalizedVdtage Scale Fator }(\mathrm{AO:01})}{\text { Vdtage Scale Fatar Divisor }(\mathrm{AO}: 02)}$
The Current and Voltage Scale Factors are written to points AO:01 through AO:04 and are stored in non-volatile memory on the Host CPU Board. Each Scale Factor is stored in two points, one for the Normalized Scale Factor, and the other for the Scale Factor Divisor. Allowable constants for Normalized Scale Factors are 1000 to 9999 . The Scale Factor Divisors may be $1,10,100$, or 1000 only.

## WARNING - TO PRESERVE SYSTEM PERFORMANCE, ONLY WRITE TO RATIO REGISTERS WHEN THE RATIOS NEED TO BE CHANGED.

### 1.5.2.1 Scale Factor Voltage Measurement Example

For example, the default full-scale value of voltage (points AI:07 to AI:20) is 150 V , the default value of the Normalized Voltage Scale Factor (AO:01) is 1000, and the default value of the Voltage Scale Factor Divisor (AO:02) is 1000. Assume a system with a 1:1 VT Ratio. If it is desired to change the full-scale representation of volts to 300 V (to accommodate a 208 V input, for instance), change the value of the Normalized Voltage Scale Factor (AO:01) to 2000.

VOLTAGE Phæe $A-B=\frac{\text { Value }}{32768} \times 150 \times \frac{2000}{1000}=300 \mathrm{~V}$
Note that since $V_{\text {SCALE }}$ FACTOR $=2$, the values represented by the power quantity points will also be doubled.

Note that the full-scale representation of all Voltage measurements will also change. The scaling for Power quantities cannot be set independently, but will be the product of the Voltage and Current Scale Factors.

### 1.5.2.2 Scale Factor Current Measurement Example

Consider a system with a 2000:5 (400:1) CT, on which it is desired to measure the Phase A amperes. The Normalized CT Ratio (AO:21) would be set to 4000, the CT Ratio Divisor (AO:22) to 10. With the default settings for the Current Scale Factor, the maximum point value of "32767" would yield:

$$
\text { AMPEREsPhase } A=\frac{\text { Value }(=32767)}{32768} \times 10 \times \frac{1000}{1000}=10 \mathrm{~A}
$$

In other words, the integer value for Amperes would be at a maximum with only 10A flowing through the system primary conductors. To compensate for this, set the I Iscale factor equal to the $\mathrm{CT}_{\text {RAtio }}$. The Normalized Current Scale Factor (AO:03) would be set to 4000, and the Current Scale Factor Divisor (AO:04) to 10. If the maximum value of "32767" is returned in point $\mathrm{Al}: 02$, it is converted to Amperes as follows:

$$
\text { AMPEREsPhase } A=\frac{\text { Value }}{32768} \times 10 \times I_{\text {SCALE FACTOR }}=\frac{32767}{32768} \times 10 \times \frac{4000}{10}=4000 \mathrm{~A}
$$

If it is known that the maximum current on the circuit is not this high, and it is desired to set the full scale representation to 1200A for added resolution, the Normalized Current Scale Factor (AO:03) could be set to 1200, and the Current Scale Factor Divisor (AO:04) to 10. The maximum value returned (32767) would then be equal to:

$$
\text { AMPEREsPhase } A=\frac{\text { Value }(=32767)}{32768} \times 10 \times \frac{1200}{10}=1200 \mathrm{~A}
$$

### 1.5.3 Resetting Energy and Demands and Triggering Waveforms

The Energy and Demand registers can be reset by issuing a CONTROL RELAY OUTPUT BLOCK to the appropriate BINARY OUTPUT. Issuing a CONTROL RELAY OUTPUT BLOCK to a "Trigger" BINARY OUTPUT will trigger a Waveform or Disturbance Record. The 70 SERIES IEDs will store the record in the next available slot. All of these registers are user-defined; they are not part of the default 70 SERIES IED register set.

Reset / Trigger Functions

| Reset Energy |
| :--- |
| Reset Demand Amps |
| Reset Demand Volts |
| Reset Demand Power |
| Reset Demand Harmonic |
| Trigger Waveform Recorder |
| Trigger Disturbance Recorder 1 |
| Trigger Disturbance Recorder 2 |

### 1.5.4 Tag Register

The 70 SERIES IEDs provide a "TAG" BINARY OUTPUT for user identification purposes. An ANALOG OUTPUT BLOCK can be issued to this point to write a number from 1 to 65,535 in the tag register.
1.5.5 VA Calculation Type Register

The 70 SERIES IEDs can be configured to use one of several different methods to calculate Total VAs. Refer to the User Manual for an explanation of the different calculation types. The VA Calculation Type register (AO:00) is a READ/WRITE register.

| VA Calculation Type | Register Value |
| :--- | :--- |
| Arithmetic | 1 |
| Geometric | 2 |
| Equivalent 3-element (WYE) | 3 |
| Equivalent 2-element (DELTA) | 4 |

### 1.6 Converting Data to Engineering Units

As mentioned in Section 1.5, the majority of the data is stored in a normalized 2's complement format. When displaying these values at another location, it may be desirable to convert this format into engineering units. This conversion is readily accomplished using the following simple scaling equations:

BASIC EQUATION FOR NORMALIZED ANALOG INPUTS:
Enginering Units $=\frac{\text { Value }}{32768} \times$ Default Full Scale $_{\text {SECONDARY }} \times \frac{\text { Normalized Scale Fadar }}{\text { Scale Fador Divisor }}$
The Value referred to in the equations would be the value stored in the point that you wished to convert to engineering units. For example if you wanted to convert Phase A Amperes into engineering units, Value would be the value in ANALOG-INPUT point.

ENERGY is stored as 32-BIT values in static COUNTER points. Energy values are in units of PRIMARY kWh or kVARh.

FREQUENCY is stored as a single binary value that is the actual frequency times 100.
POWER FACTOR is stored as the value times 1000. Negative power factors indicate that the VARs are positive. The sign of the Power Factor is the inversion of the Exclusive-OR of the Watts and VARs (i.e. if either or both of the Watts or VARs are negative, then the Power Factor will be negative).

EQUATIONS FOR FIXED DATA POINT SET:

$$
\begin{aligned}
& I_{\text {SCALE FACTOR }}=\frac{\text { Normalized Current Scale Factor (AO:03) }}{\text { Current Scale Factor Divisar (AO:04) }} \\
& V_{\text {SCALE FACTOR }}=\frac{\text { NormalizedVdtage ScaleFacta }(A O: 01)}{\text { Vdtage ScaleFadar Divisor }(A O: 02)} \\
& A^{\text {APERE }} S_{(\text {Ins, Demend, Max) })}=\frac{\text { Value }}{32768} \times 10 \times I_{\text {SCALE FACTOR }} \\
& \text { AMPERES } S_{V} \text { (Ins, Demend, Max) }=\frac{\text { Value }}{32768} \times 15 \times I_{\text {SCALE FACTOR }}
\end{aligned}
$$

$$
\begin{aligned}
& \text { WATTs(VARS) }(\text { VAS })_{\text {TOTAL (Ins, Demend, Max, Max) }}=\frac{\text { Value }}{32768} \times 4500 \times V_{\text {SCALE FACTOR }} \times I_{\text {SCALE FACTOR }} \\
& \text { WATTs(VARs) (VAs) })_{\text {PER PHASE(ns) })}=\frac{\text { Value }}{32768} \times 1500 \times V_{\text {SCALE FACTOR }} \times I_{\text {SCALE FACTOR }} \\
& k W h(k V A R h)=\text { Value } \\
& \text { FREQUENCY }=\frac{\text { Value }}{100} \\
& P F=\frac{\text { Value }}{1000}(-L a g,+ \text { Læd }) \\
& \text { PHASE DIFFERENCE }=\frac{\text { Value }}{10}(+ \text { LineLeqdingR ef })
\end{aligned}
$$

All quantities reported in Primary Values. To force the 70 SERIES IEDs to report in secondary units, set the Scale Factor = to the CT or VT ratio, as appropriate.

The above equations provide answers in fundamental units (VOLTs, AMPs, WATTs, VARs, VAs and Hz). If the user desires other units such as KILOVOLTS, KILOWATTS or KILOVARS, the answers given by the equations should be divided by 1,000 . If the user desires MEGAWATTS or MEGAVARS, the answers given by the equations should be divided by 1,000,000. Energy values are in units of kWh or kVARh.

### 1.7 Data Sets and Data Types

The 70 SERIES IEDs are shipped with a pre-defined set of data points and data types. These fixed points do not change, but may be augmented by adding additional points (and their data type), from the master listing. The List of Available Measurements may be found in the User Manual. The 70 SERIES Configurator is required to modify the data points.

### 1.7.1 Configuring the Class-0 Response

The Class-0 request is essentially a request to "give all data". Since the 70 SERIES IEDs is capable of providing a vast amount of data, provisions have been made to limit the response to this request. The 70 SERIES Configurator is required to change the Class-0 response. The Legacy Class-0 response is also configurable, and can return one of six pre-defined responses.
1.7.2 Configuring Class-1, Class-2 and Class-3 Events

BINARY INPUT points become BINARY INPUT CHANGE events by assigning the point to either the CLASS-1, CLASS-2, or CLASS-3 Data Object. ANALOG INPUT points become ANALOG CHANGE EVENTs by assigning the point to either the CLASS-1, CLASS-2, or CLASS-3 Data Object. This assignment is accomplished by simply moving the desired point(s) into the associated CLASS Data Object block via the 70 SERIES Configurator utility software (DNP Points configuration section).

When assigning ANALOG INPUTS a DEADBAND value must also be entered such that the ANALOG CHANGE EVENT will be generated whenever the last reported value changes by more than that DEADBAND amount. Any point may exist in any CLASS but it may only exist in one CLASS. All BINARY INPUTs can be assigned to BINARY INPUT CHANGE events. The 70 SERIES IEDs permit a maximum of forty ANALOG INPUTs to be assigned to ANALOG INPUT CHANGE events.
1.7.3 Time Sync

Pre-defined data points for status are used to indicate the current state for each of the various time synchronization methods possible on the 70 Series IEDs. These data points appear in the Bitronics Advanced Fixed (BAF) and Harmonic Advance Fixed (HAF) point sets. The 70 Series Configurator allows the user to modify the configuration of time sync parameters.

The following time sync points will return status values of ' 0 ' if a time sync master is inactive and ' 1 ' if a time sync master is active:

IRIG-B Time Sync, (UCA) Network Time Sync, SNTP Time Sync, DNP Time Sync.

### 1.7.4 Best Clock Source

The M87x or M57x determines the 'Best Clock Source' and returns a value to indicate the master that is synchronizing the time. This is based upon which time sync masters are active as determined from the Time Sync Data Points and which time sync master takes priority.

| Best Clock source | Value |
| :--- | :--- |
| IRIG-B: | $\mathbf{2}$ |
| (UCA) Network Time <br> Sync | $\mathbf{3}$ |
| SNTP | $\mathbf{4}$ |
| DNP | $\mathbf{5}$ |
| Manual time set | $\mathbf{0}$ |

Refer to the IED User Manuals (M87x or M57x) for additional information on Time Sync clock source priority.

### 1.8 Health Check

The 70 SERIES IEDs have several self-tests built in to ensure that the instrument is performing accurately. The results of these self-tests are available in the Health Check register (AO:00), which is a simple 16-bit binary value. Each bit represents the results of a particular self-test, with " 0 " indicating the test was passed, and "1" indicating the test was failed. The definitions of the various self-tests are described in the User Manual. The following table lists possible faults that would be detected by the self-tests, how the fault is indicated, the effects of the fault, and any necessary corrective actions.

| Bit \# | Description | Effect | Default Value |
| :---: | :---: | :---: | :---: |
| O(LSB) | Factory gain calibration of Analog-Digital Signal Processor checksum error. | Unit will continue to function using default values, at reduced accuracy. | A/D Gain = 1 |
| 1 | Factory offset calibration of Analog-Digital Signal Processor Module checksum error. | Unit will continue to function using default values, at reduced accuracy. | A/D Offset $=0$ |
| 2 | Factory gain calibration of Signal Input Module checksum error. | Unit will continue to function using default values, at reduced accuracy. | CT/VT Gain $=1$ |
| 3 | Factory offset calibration of Signal Input Module checksum error. | Unit will continue to function using default values, at reduced accuracy. | CT/VT Offset $=0$ |
| 4 | Factory phase calibration of Signal Input Module checksum error. | Unit will continue to function using default values, at reduced accuracy. | CT/VT Phase $=0$ |
| 5 | Factory defined internal ratios of Signal Input Module checksum error. (Type of Signal Input Module) | Unit will continue to function. Assumes -S10 Signal Input Module | Volts Ratio = 60 :1 <br> Amps Ratio = $14.136: 1$ |
| 6 | User defined external transformer ratio checksum error. | Unit will continue to function using default values (i.e. w/o user ratios). | User CT = 5:5, VT = 1:1 |
| 7 | User gain correction values checksum error. | Unit will continue to function using default values (i.e. w/o user gain). | User Gain = 1 |
| 8 | User phase correction values checksum error. | Unit will continue to function using default values (i.e. w/o user phase). | User Phase $=0$ |
| 9 | Factory defined board ID for Analog-Digital Signal Processor Module checksum error. | Assumes default Analog-Digital Signal Processor Module. | Module -A10 (M87x) |
| 10 | Factory defined board ID for Signal Input Module checksum error. | Assumes default Signal Input Module. | Module -S1x (M87x) |
| 11 | User defined denominators for TDD measurement checksum error. | Assumes default TDD Denominator. | TDD Denom $=5 \mathrm{~A}$ Secondary |
| 12 | DSP program integrity checksum error. | Host trips watchdog, unit reboots. |  |
| 13 | DSP stack overflow. | Host trips watchdog, unit reboots. |  |
| 14 | Invalid or missing Amp and/or Voltage Scale Factor. | Protocol will use default Scale Factor | Scale Factor = 1:1 |
| 15 | Protocol configuration invalid. | IED uses default protocol configuration | 70 SERIES default register set |

### 1.9 Diagnostic Status LED

The Diagnostic LED is an indicator that shows the communications activity on the DNP port of the 70 SERIES IEDs. The Diagnostic LED is a bi-color LED (red/green) indicator that is located on the Front Panel Board adjacent to each serial port. The Diagnostic LED will flash red every time the 70 SERIES IED receives data via the associated port and will flash green whenever the 70 SERIES IED sends data over the associated serial port. If the LED does not flash RED when a message is sent to it from a MASTER, check the network for the following problems:

1. Cable open or short circuit
2. Defective termination
3. Incorrect DNP ADDRESS
4. Incorrect polarity of cable connections

### 1.10 Heartbeat State Counter

The 70 SERIES IEDs provide a Heartbeat State Counter Register that allows the user to determine the time between successive polls. This counter will increment by the number of milliseconds that have elapsed since the last time the data was updated. Another use of this register is as a visual indicator that the data is changing; it allows users of certain MMIs to identify disruption in the polling of the instrument. The Heartbeat State Counter is a full 32bit counter that rolls over at 4,294,967,295 ( $4,294,967$ seconds). The counter starts at zero on power-up, and is NOT stored in non-volatile memory.

### 1.11 Meter ID Register

The 70 SERIES IEDs provides an "ID" register for model identification purposes (AI:53).

## 2. DNP PROTOCOL

### 2.1 Introduction

DNP 3.0 (Distributed Network Protocol) is an open standard that was designed by Harris Controls Division and then placed in the public domain. DNP defines a command-response method of communicating digital information between a master and slave device. The electrical connection between devices is known as a bus. In DNP, two types of devices attach to the bus: master and slave devices. A master device issues commands to slaves. A slave device, such as a 70 SERIES IED, issues responses to master commands that are addressed to them. Each bus must contain exactly one master and may contain as many slaves as the electrical standards permit.

All devices on a bus must operate according to the same electrical standards (i.e. all must be RS-232C or all must be RS-485). RS-232C standards specify that only two devices may be connected to a bus (i.e. only one slave is allowed). RS-485 specifications allow up to 32 devices (31 slaves) on a bus.

Detailed information regarding DNP 3.0 is available in a document titled "Basic 4 Document Set" which can be obtained from the DNP Users Group. The remainder of this chapter provides a brief overview of the protocol as implemented in the 70 SERIES IEDs.

### 2.2 Overall Protocol Structure

DNP is a 3-layer protocol based upon the standard IEC 870-5 (Telecontrol Equipment and Systems - Transmission Protocols). The three layers comprise the Enhanced Performance Architecture (EPA) and is a subset of the more familiar ISO-OSI 7-layer protocol. The three layers are the physical, data link, and application layers. The physical layer is responsible for transmission of raw 8 -bit bytes (octets) across the network medium. The data link layer is responsible for reliably maintaining connectivity between two devices. The application layer defines standardized messages that flow between devices. DNP further defines an extra layer known as the transport layer that allows very long messages to be broken down into smaller pieces.

### 2.3 DNP Request/Response Overview

The 70 SERIES IEDs DNP implementation supports a wide variety of messages. The most general method to extract information from a 70 SERIES IED is to issue a READ CLASS-0 request. DNP devices respond with the points to be returned in the Class-0 response. See Section 1.71 for more details on Class-0 configuration. This allows the MASTER to retrieve all readings from the instrument and determine whether the output points are online (i.e. whether energy/demand resets or ratio setup requests can be honored). The 70 SERIES IEDs also allow READs of individual objects specifying all points (variation 6) or individual points (other variations). The 70 SERIES IEDs execute the energy clear function and demand resets using the DIRECT OPERATE, DIRECT OPERATE NO ACKNOWLEDGE, or SELECT BEFORE OPERATE functions to the CONTROL RELAY OUTPUT BLOCK object points. CT/PT ratio setups are made via DIRECT OPERATE, DIRECT OPERATE NO ACKNOWLEDGE, or SELECT BEFORE OPERATE to the ANALOG OUTPUT BLOCK object points. Configuration setups are also made via the DIRECT OPERATE, DIRECT OPERATE NO ACKNOWLEDGE, or SELECT BEFORE OPERATE object. The DNP function code WRITE is also supported by the 70 SERIES IEDs.

A 70 SERIES IED will attempt to respond with the same object variation and qualifier as in the request. Exceptions to this rule include changing variation 0 to a specific variation, and changing qualifier code 6 to 0 or 6 to 1 .

## 3. DNP3 over ETHERNET (TCP)

If the 70 SERIES IED is equipped with one of the Ethernet options, then it will respond to DNP3 commands via TCP. The 70 SERIES IEDs can simultaneously support DNP3, Modbus, and UCA2 protocols over the Ethernet link. The table below lists port assignments for all Ethernet based protocols supported by the 70 Series.

| PROTOCOL | PORT NUMBER |
| :--- | :--- |
| DNP | 20000 (TCP, UDP) |
| FTP (recommend passive mode) | 20,21 (TCP) |
| Modbus | 502 (TCP) |
| MMS (UCA \& 61850) | 102 (TCP) |
| SMTP (electronic mail) | 25 (TCP) |
| SNTP (network time synch) | 123 (UDP) |
| Telnet | 23 (TCP) |

### 3.1 DNPIIP

The DNP/IP (DNP Over TCP/IP and DNP Over UDP/IP) interface allows up to 16 DNP Masters to communicate with the 70 SERIES IEDs. Each remote IP host (Client) may communicate with the 70 SERIES Server via UDP or TCP. A Client may have multiple DNP Master addresses. Each Client has a distinct set of DNP Master addresses. Up to five filters for acceptable remote IP addresses may be set up (these may include wild-cards). The 70 SERIES IEDs reject requests from an IP address that is not included in the filter list.

Any DNP Master address will be accepted. Any DNP Slave address will be accepted with the restriction that all DNP Slave addresses from any particular Client must be the same.

After establishment of a TCP connection from a DNP Client, the 70 SERIES IEDs attempt to maintain contact by periodically sending REQUEST LINK STATUS messages. The device expects that each Master will maintain contact either by periodically polling the 70 SERIES IEDs or by responding to the REQUEST LINK STATUS messages with a RESPOND message. These messages are used as keep-alive messages.

DNP/IP has the following configuration parameters:
Send Keep Alive: This controls the minimum interval (in seconds) between outgoing messages from the 70 SERIES IEDs. If no data messages have been sent for this interval, a keep-alive message is sent. A value of zero stops the 70 SERIES IEDs from initiating keep-alive messages. A suggested default value for this is 10 .

Max Send: $\quad$ This controls the number of keep-alive messages that will be sent without receiving a response from the Master before it is declared unresponsive, and the TCP connection closed. A suggested default for this value is 10 .

Receive Keep Alive: This controls the maximum time between messages (in seconds) from the Master before the Master is declared unresponsive. When the 70 SERIES IEDs discover that all Masters on a TCP connection are unresponsive, the IED will close the TCP connection. Setting this value to zero stops the 70 SERIES IEDs from declaring Masters dead based on lack of messages from them. A suggested default for this value is either 0 or 30 .

Num IP Filters: The number of IP filter values. It ranges from 0 to 5 where zero means that all IP addresses are accepted by the 70 SERIES IEDs. An IP address is accepted if it passes any filter.

IP Filter[0]: | The value of first acceptable Internet Protocol filter in the form of |
| :--- |
| "dotted decimal" notation. For example, the filter "192.168.0.1" |
| (without quotes) would allow exactly one address through the filter |
| and "192.168.*.*" would allow any address beginning with 192.168 to |
| be accepted. A value of "......." would allow all addresses to be |
| accepted. |

IP Filter[1] through IP Filter[4]:
Additional filter values.

### 3.1.1 IP Addressing

The TCP/IP stack needs to be configured with an IP address, a SUBNET mask, and a ROUTER (GATEWAY) address. It is very important that the network have no duplicate IP addresses. Configuration of the address may be accomplished by using UCA, by running the 70 SERIES Configurator, or via a front panel serial port using a terminal emulator such as HyperTerminal ${ }^{\text {TM }}$ or ProComm ${ }^{\text {TM }}$.

The units are pre-configured with an IP address / subnet mask/gateway address of:
192.168.0.254 | 255.255.255.0 / 192.168.0.1

DNP/IP Quick Tutorial
DNP/IP uses the concept of a Virtual Serial Port (VSP). An IP Client communicates with DNP/IP in the same way that a Client with a serial port would communicate over the DNP serial asynchronous protocol. Both UDP and TCP are "piped" into the 70 SERIES IEDs. UDP messages are examined for the source IP address and automatically attached to the physical connection that it previously used. TCP messages behave exactly the same way. Note that the source IP port number is not used in any way. TCP messages are sent over pre-established IP connections. The connection attachment request is accepted only if the connection attached to that IP address is unpopulated (it could have previously been attached to either a UDP or TCP port). TCP connections are de-populated (disconnected) under two conditions: (1) disconnection requests by the Client or (2) discovery that all Masters on the connection are unresponsive. A Client disconnection request is treated exactly as if every Master on the connection has become unresponsive. Note that TCP disconnection does not disassociate the individual Master addresses on that connector. The only way that this disassociation takes place is when the number of Client/Master address pairs exceeds 16. In this case, the Master that has least recently been sent a message is disassociated from the Client. Note that UDP communications are transient, they behave as if they were TCP connect/transaction/disconnect groups.

UDP and TCP share the same Virtual Serial Port (VSP). Upon a TCP disconnect, the UDP can take over all Master sessions. A TCP connection request can always take over for UDP data gram flow.

## 4. FILE TRANSFER

The 70 SERIES IEDs support the DNP file transfer implementation as described in the "Sequential File Transfer Objects" DNP Technical Bulletin. This document is a replacement for file transfer as described in the Basic 4 document set and is available from the DNP Users Group.

## 5. DNP3 EVENTS OVERVIEW

DNP3 provides for a method of reporting data only when it may be of interest to the application. This can significantly reduce the network bandwidth required by eliminating the redundant polling of data and only polling data when it changes enough to be consider relevant. When a change in a particular data value becomes relevant to the application, that change is called an event.

Events are pre-assigned to one of three CLASSes, (CLASS-1, CLASS-2, or CLASS-3). When an event occurs, the data point and OBJECT type are placed in a buffer and the event's specific CLASS BIT (BIT1, BIT2, or BIT3 of the first IIN octet) is set in the 70 SERIES IED's Internal Indications (IIN) field. DNP3 master devices monitor the IIN bits and will issue a specific CLASS-1, CLASS-2, or CLASS-3 poll when the respective CLASS IIN bit is set. The 70 SERIES IED will respond to the specific CLASS poll with all data buffered for the CLASS requested and than clear the associated CLASS IIN bit.

### 5.1 BINARY INPUT CHANGE Events

BINARY INPUT CHANGE events occur when a BINARY INPUT that is assigned to a CLASS-1, CLASS-2, or CLASS-3 Data Object changes states. Once the BINARY INPUT changes states, the specific BINARY INPUT point number and the new state value are placed in the BINARY INPUT EVENT BUFFER as a BINARY INPUT CHANGE event. 70 SERIES IEDs can be configured to report The BINARY INPUT CHANGE data object in one of two VARATIONs, either BINARY INPUT CHANGE WITHOUT TIME (object 02, variation 1) or BINARY INPUT CHANGE WITH TIME (object 02, variation 2). All BINARY INPUT CHANGE events will be reported with the same configured variation. The default BINARY INPUT CHANGE variation (with or without time) can be set using the 70 SERIES Configurator utility software.

The 70 SERIES IEDs allocate the BINARY INPUT EVENT BUFFER size based on the number of configured DNP3 BINARY INPUTs. The buffer size is equivalent to 5 times the number of BINARY INPUTS. For example, a 70 SERIES IED with eight BINARY INPUTs would be able to buffer forty BINARY INPUT CHANGE events. If the buffer size is exceeded prior to being emptied by the CLASS poll, the oldest BINARY INPUT CHANGE event is purged from the buffer and is lost in order to make room for the most recent event. This buffer overflow status is reported to the DNP3 master by setting the BIT:3 of the second IIN octet in the Internal Indications field.

### 5.2 ANALOG CHANGE Events

ANALOG CHANGE events occur when an ANALOG INPUT that is assigned to a CLASS-1, CLASS-2, or CLASS-3 Data Object changes by more than its configured DEADBAND value since the last time it was reported. Once the ANALOG INPUT's value changes by more than the configured DEADBAND, the specific ANALOG INPUT point number and the new value are placed in the ANALOG CHANGE EVENT BUFFER as an ANALOG CHANGE EVENT. 70 SERIES IEDs can be configured to report The ANALOG CHANGE EVENT Data Object in one of two Variations, either 16-BIT ANALOG CHANGE EVENT WITHOUT TIME (object 32, variation 2) or 16-BIT ANALOG CHANGE EVENT WITH TIME (object 32, variation 4). All ANALOG CHANGE EVENTS will be reported with the same configured VARIATION. The default ANALOG CHANGE EVENT variation (with or without time) can be set using the 70 SERIES Configurator utility software. The Configurator software is also used to set the DEADBAND value for each configured ANALOG CHANGE EVENT.

The 70 SERIES IEDs allocate the ANALOG CHANGE EVENT BUFFER size based on the number of configured DNP3 ANALOG INPUTs. The buffer size is equivalent to one more than number of configured ANALOG INPUTS. For example, a 70 SERIES IED with sixty four ANALOG INPUTs would be able to buffer sixty five ANALOG CHANGE events. If the buffer size is exceeded prior to being emptied by the CLASS poll, the oldest ANALOG CHANGE event is purged from the buffer and is lost in order to make room for the most recent event. This buffer overflow status is reported to the DNP3 master by setting the BIT:3 of the second IIN octet in the Internal Indications field.

The 70 SERIES IEDs allow for the ANALOG CHANGE EVENTs to operate in one of two modes, SOE mode or PRESENT mode. In SOE (Sequence of Events) mode, the ANALOG CHANGE EVENT buffering operates identical to the BINARY INPUT CHANGE events. In SOE mode all events remain in the ANALOG CHANGE EVENT BUFFER until the CLASS
poll retrieves them or in the case of a buffer overflow they are forced out by newer events. In PRESENT mode, before a new ANALOG CHANGE EVENT is placed in the ANALOG CHANGE EVENT BUFFER, the buffer is checked to see if any other ANALOG CHANGE EVENTS are present in the buffer for the same point. If there are, the older ANALOG CHANGE EVENT (for the same point) is removed regardless of how much space remains in the buffer. The ANALOG CHANGE EVENT buffering mode is selectable by the 70 SERIES Configurator utility software.

## 6. DNP FROZEN COUNTER OBJECTS

The 70 Series IEDs with firmware v1.27 or later support the DNP3 FROZEN COUNTER Object. Each DNP3 BINARY COUNTER (OBJECT 20) Point configured will automatically have an associated FROZEN COUNTER (OBJECT 21) Point configured. The BINARY COUNTER and its associated FROZEN COUNTER will have the same point number (FROZEN COUNTER Point '0' will contain the value frozen from BINARY COUNTER Point '0').

The 70 Series IEDs will support the IMMEDIATE FREEZE (FC-07), IMMEDIATE FREEZE NO ACKNOWLEDGEMENT (FC-08), FREEZE AND CLEAR (FC-09), and FREEZE AND CLEAR - NO ACKNOWLEDGEMENT (FC-10) Function Codes. Freeze commands and FROZEN COUNTERS can use any of the same QUALIFIERS and VARIATIONS as the BINARY COUNTERS.

Each DNP3 master communication session will have its own unique set of FROZEN COUNTERs. Once a DNP3 master initiates communication with the 70 Series IED, a communications session is established. This communication session allocates a dedicated set of FROZEN COUNTERs specifically for the new DNP3 master. When the new DNP3 master issues a FREEZE or FREEZE AND CLEAR command, only the FROZEN COUNTERs allocated for that DNP3 master are frozen (and cleared depending on the command). This allows multiple masters to maintain their own FROZEN COUNTERS and prevents one DNP3 master from inadvertently clearing another DNP3 master's counter(s).

Upon session initialization, the FROZEN COUNTERS are initialized to zero. The FROZEN COUNTERs will continue to be read as zero until a FREEZE command is sent by the associated DNP3 master. If a FREEZE AND CLEAR command is sent the present count value will be stored in the FROZEN COUNTER and the running count of the FROZEN COUNTER value will be reset. By default, the FREEZE AND CLEAR COMMAND will NOT clear the associated BINARY COUNTER values. The BINARY COUNTER values for energy values will by default always match the energy measurements stored in the 70 Series IED database. This ensures that the energy measurements will always be reported the same regardless of protocol, port, or master device.

A hidden protocol configuration mode can be changed that modifies the way the FREEZE AND CLEAR command works. If the mode is changed the 70 Series IED will clear out the associated running counter and BINARY COUNTER when the FREEZE AND CLEAR command is issued. When 70 Series IEDs are configured to operate in this mode and one DNP3 master issues a FREEZE AND CLEAR command, the energy measurements read from the BINARY COUNTERs will not longer match the energy measurements read by any other master (DNP3, Modbus, ModbusPlus, UCA) communicating with the same instrument. In this mode, the BINARY COUNTERs will be initialized and synchronized to the true energy measurements until the first FREEZE AND CLEAR command. After the first FREEZE AND CLEAR command synchronization to the true energy measurements is lost.

All FROZEN COUNTERs are volatile. As previously mentioned, the FROZEN COUNTERs are initialized to zero at start up. Any FROZEN COUNTERS that are part of a re-started communication session are initialed to zero as well. Any values frozen but not read prior to the 70 Series IED restarting or that were not read prior to the communication session closing are lost. BINARY COUNTERs are also resynchronized with the energy measurement values at system start up and session start up.

Sessions are closed automatically when the number of sessions per port is exceeded. Each serial port supports one communication session (or one DNP3 master). The 70 Series IED supports eighteen DNP3 communication sessions via Ethernet. If a message from a new DNP3 master is detected and the number of sessions for the associated port has been exceeded the existing communication session with the oldest activity for the associated port is closed (all FROZEN COUNTER values lost) and a new communications session is started (new FROZEN COUNTERs initialized to zero). The new master is identified on the serial ports by having a different SOURCE ADDRESS in the DATA LINK LAYER of the DNP3 command. The new DNP3 Ethernet masters are determined by different IP address.

## APPENDIX A DNP3 POINT ASSIGNMENTS FOR DFC AND BAF POINT SETS

| DFC (Dual Feeder Configurable) |  |  |
| :--- | :--- | :--- |
|  | Analog Inputs | VT 1 Scale Factor |
| 0 | Health | VT 1 Scale Factor |
| 1 | Health | CT 1 Scale Factor |
| 2 | Register Set | CT 1 Scale Factor |
| 3 | Meter Type | VT 2 Scale Factor |
| 4 | Firmware Version | VT 2 Scale Factor |
| 5 | RMS Amps A 1 | CT 2 Scale Factor |
| 6 | RMS Amps B 1 | CT 2 Scale Factor |
| 7 | RMS Amps C 1 | Xfmr Ratio Volts A 1 |
| 8 | RMS Amps A 2 | Xfmr Ratio Volts A 1 |
| 9 | RMS Amps B 2 | Xfmr Ratio Volts B 1 |
| 10 | RMS Amps C 2 | Xfmr Ratio Volts B 1 |
| 11 | RMS Volts A 1 | Xfmr Ratio Volts C 1 |
| 12 | RMS Volts B 1 | Xfmr Ratio Volts C 1 |
| 13 | RMS Volts C 1 | Xfmr Ratio Volts N 1 |
| 14 | RMS Volts N 1 | Xfmr Ratio Volts N 1 |
| 15 | RMS Volts AB 1 | Xfmr Ratio Volts A 2 |
| 16 | RMS Volts BC 1 | Xfmr Ratio Volts A 2 |
| 17 | RMS Volts CA 1 | Xfmr Ratio Volts B 2 |
| 18 | RMS Volts A 2 | Xfmr Ratio Volts B 2 |
| 19 | RMS Volts B 2 | Xfmr Ratio Volts C 2 |
| 20 | RMS Volts C 2 | Xfmr Ratio Volts C 2 |
| 21 | RMS Volts N 2 | Xfmr Ratio Volts N 2 |
| 22 | RMS Volts AB 2 | Xfmr Ratio Volts N 2 |
| 23 | RMS Volts BC 2 |  |
|  |  |  |


| DFC (Dual Feeder Configurable) <br> Analog Inputs |  | Analog Outputs |
| :--- | :--- | :--- |
| 24 | RMS Volts CA 2 | Xfmr Ratio Volts R 1 |
| 25 | RMS Volts R 1 | Xfmr Ratio Volts R 1 |
| 26 | RMS Volts R 2 | Xfmr Ratio Volts R 2 |
| 27 | RMS Watts A 1 | Xfmr Ratio Volts R 2 |
| 28 | RMS Watts B 1 | Xfmr Ratio Amps A 1 |
| 29 | RMS Watts C 1 | Xfmr Ratio Amps A 1 |
| 30 | RMS Watts Total 1 | Xfmr Ratio Amps B 1 |
| 31 | RMS VARs A 1 | Xfmr Ratio Amps B 1 |
| 32 | RMS VARs B 1 | Xfmr Ratio Amps C 1 |
| 33 | RMS VARs C 1 | Xfmr Ratio Amps C 1 |
| 34 | RMS VARs Total 1 | Xfmr Ratio Amps N 1 |
| 35 | RMS VAs A 1 | Xfmr Ratio Amps N 1 |
| 36 | RMS VAs B 1 | Xfmr Ratio Amps A 2 |
| 37 | RMS VAs C 1 | Xfmr Ratio Amps A 2 |
| 38 | RMS VAs Total 1 | Xfmr Ratio Amps B 2 |
| 39 | Power Factor A 1 | Xfmr Ratio Amps B 2 |
| 40 | Power Factor B 1 | Xfmr Ratio Amps C 2 |
| 41 | Power Factor C 1 | Xfmr Ratio Amps C 2 |
| 42 | Power Factor Total 1 | Reserved for Xfmr Ratio Amps N 2 |
| 43 | RMS Watts A 2 | Reserved for Xfmr Ratio Amps N 2 |
| 44 | RMS Watts B 2 | User Gain Volts A 1 |
| 45 | RMS Watts C 2 | User Gain Volts B 1 |
| 46 | RMS Watts Total 2 | User Gain Volts C 1 |
| 47 | RMS VARs A 2 | User Gain Volts N 1 |
| 48 | RMS VARs B 2 | User Gain Volts A 2 |
| 49 | RMS VARs C 2 | User Gain Volts B 2 |


|  | DFC (Dual Feeder Configurable) |  |
| :---: | :---: | :---: |
|  | Analog Inputs | Analog Outputs |
| 50 | RMS VARs Total 2 | User Gain Volts C 2 |
| 51 | Reserved (returns 0) | User Gain Volts N 2 |
| 52 | Reserved (returns 0) | User Gain Volts R 1 |
| 53 | Meter Type | User Gain Volts R 2 |
| 54 | Reserved (returns 0) | User Gain Amps A 1 |
| 55 | Meter Type | User Gain Amps B 1 |
| 56 | RMS VAs A 2 | User Gain Amps C 1 |
| 57 | RMS VAs B 2 | User Gain Amps N 1 |
| 58 | RMS VAs C 2 | User Gain Amps A 2 |
| 59 | RMS VAs Total 2 | User Gain Amps B 2 |
| 60 | Power Factor A 2 | User Gain Amps C 2 |
| 61 | Power Factor B 2 | Reserved for User Gain Amps N2 |
| 62 | Power Factor C 2 | User Phase Correction Volts A 1 |
| 63 | Power Factor Total 2 | User Phase Correction Volts B 1 |
| 64 | Frequency Volts A 1 | User Phase Correction Volts C 1 |
| 65 | Frequency Volts B 1 | User Phase Correction Volts N 1 |
| 66 | Frequency Volts C 1 | User Phase Correction Volts A 2 |
| 67 | Frequency Volts A 2 | User Phase Correction Volts B 2 |
| 68 | Frequency Volts B 2 | User Phase Correction Volts C 2 |
| 69 | Frequency Volts C 2 | User Phase Correction Volts N 2 |
| 70 | Frequency Volts R 1 | User Phase Correction Volts R 1 |
| 71 | Frequency Volts R 2 | User Phase Correction Volts R 2 |
| 72 | System Frequency | User Phase Correction Amps A 1 |
| 73 | Reserved for System Frequency 2 | User Phase Correction Amps B 1 |
| 74 | Phase Angle Volts A 1-2 | User Phase Correction Amps C 1 |
| 75 | Phase Angle Volts B 1-2 | User Phase Correction Amps N 1 |


|  | DFC (Dual Feeder Configurable) |  |
| :---: | :---: | :---: |
|  | Analog Inputs | Analog Outputs |
| 76 | Phase Angle Volts C 1-2 | User Phase Correction Amps A 2 |
| 77 | Phase Angle Volts A 1 - R 1 | User Phase Correction Amps B 2 |
| 78 | Phase Angle Volts B 1 - R 1 | User Phase Correction Amps C 2 |
| 79 | Phase Angle Volts C 1 - R 1 | Reserved for User Phas Correction Amps N 2 |
| 80 | Phase Angle Volts A 1 - R 2 | VA/PF Calc. Type |
| 81 | Phase Angle Volts B 1 - R 2 | Reserved (returns 0) |
| 82 | Phase Angle Volts C 1-R 2 | Reserved (returns 0) |
| 83 | Phase Angle RMS Amps A 1 Harmonic 01 | Reserved (returns 0) |
| 84 | Phase Angle RMS Amps B 1 Harmonic 01 | Reserved (returns 0) |
| 85 | Phase Angle RMS Amps C 1 Harmonic 01 | Reserved (returns 0) |
| 86 | Phase Angle RMS Amps A 2 Harmonic 01 | Reserved (returns 0) |
| 87 | Phase Angle RMS Amps B 2 Harmonic 01 | Reserved (returns 0) |
| 88 | Phase Angle RMS Amps C 2 Harmonic 01 | Reserved (returns 0) |
| 89 | Phase Angle RMS Volts A 1 Harmonic 01 | Reserved (returns 0) |
| 90 | Phase Angle RMS Volts B 1 Harmonic 01 | Reserved (returns 0) |
| 91 | Phase Angle RMS Volts C 1 Harmonic 01 | Reserved (returns 0) |
| 92 | Phase Angle RMS Volts A 2 Harmonic 01 | Reserved (returns 0) |
| 93 | Phase Angle RMS Volts B 2 Harmonic 01 | Reserved (returns 0) |
| 94 | Phase Angle RMS Volts C 1 Harmonic 01 | Reserved (returns 0) |
| 95 | Reserved (returns 0) | Reserved (returns 0) |
| 96 | Reserved (returns 0) | Reserved (returns 0) |
| 97 | Reserved (returns 0) | Reserved (returns 0) |
| 98 | Reserved (returns 0) | Reserved (returns 0) |
| 99 | Reserved (returns 0) |  |
| 100 | Reserved (returns 0) |  |
| 101 | Reserved (returns 0) |  |


| DFC (Dual Feeder Configurable) |  |  |
| :--- | :--- | :--- |
|  | Analog Inputs | Analog Outputs |
| 102 | Reserved (returns 0) |  |
| 103 | Reserved (returns 0) |  |
| 104 | Reserved (returns 0) |  |
| 105 | Reserved (returns 0) |  |
| 106 | Reserved (returns 0) |  |
| 107 | Reserved (returns 0) |  |
| 108 | Reserved (returns 0) |  |
| 109 | Reserved (returns 0) |  |
| 110 | Reserved (returns 0) |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 00 | Health | VT 1 Scale Factor | Reserved (returns 0) | DIO\#0 Status Output Point 1 | Heartbeat |
| 001 | Health | VT 1 Scale Factor | Reserved (returns 0) | DIO\#0 Status Output Point 2 | KWatt1-Hrs Normal |
| 002 | Register Set | CT 1 Scale Factor | Reserved (returns 0) | DIO\#0 Status Output Point 3 | KWatt1-Hrs Reverse |
| 003 | Meter Type | CT 1 Scale Factor | Reserved (returns 0) | DIO\#O Status Output Point 4 | KVAR1-Hrs Lag |
| 004 | Firmware Version | VT 2 Scale Factor | Reserved (returns 0) | DIO\#1 Status Output Point 1 | KVAR1-Hrs Lead |
| 005 | RMS Volts A 1 | VT 2 Scale Factor | Reserved (returns 0) | DIO\#1 Status Output Point 2 | KWatt2-Hrs Normal |
| 006 | RMS Volts B 1 | CT 2 Scale Factor | Reserved (returns 0) | DIO\#1 Status Output Point 3 | KWatt2-Hrs Reverse |
| 007 | RMS Volts C 1 | CT 2 Scale Factor | Reserved (returns 0) | DIO\#1 Status Output Point 4 | KVAR2-Hrs Lag |
| 008 | RMS Volts N 1 | VA/PF Calc. Type | Reserved (returns 0) | DIO\#2 Status Output Point 1 | KVAR2-Hrs Lead |
| 009 | RMS Volts R 1 | Xfmr Ratio Volts A 1 | Reserved (returns 0) | DIO\#2 Status Output Point 2 | Reserved (returns 0) |
| 010 | RMS Volts R 2 | Xfmr Ratio Volts A 1 | Reserved (returns 0) | DIO\#2 Status Output Point 3 | Reserved (returns 0) |
| 011 | RMS Volts AB 1 | Xfmr Ratio Volts B 1 | Reserved (returns 0) | DIO\#2 Status Output Point 4 | Reserved (returns 0) |
| 012 | RMS Volts BC 1 | Xfmr Ratio Volts B 1 | Reserved (returns 0) | DIO\#3 Status Output Point 1 | Reserved (returns 0) |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 013 | RMS Volts CA 1 | Xfmr Ratio Volts C 1 | Reserved (returns 0) | DIO\#3 Status Output Point 2 | Reserved (returns 0) |
| 014 | RMS Amps A 1 | Xfmr Ratio Volts C 1 | Reserved (returns 0) | DIO\#3 Status Output Point 3 | Reserved (returns 0) |
| 015 | RMS Amps B 1 | Xfmr Ratio Volts N 1 | Reserved (returns 0) | DIO\#3 Status Output Point 4 | Reserved (returns 0) |
| 016 | RMS Amps C 1 | Xfmr Ratio Volts N 1 | Reserved (returns 0) | DIO\#4 Status Output Point 1 | Reserved (returns 0) |
| 017 | RMS Amps N 1 | Xfmr Ratio Amps A 1 | Reserved (returns 0) | DIO\#4 Status Output Point 2 | Reserved (returns 0) |
| 018 | RMS Volts A 2 | Xfmr Ratio Amps A 1 | Reserved (returns 0) | DIO\#4 Status Output Point 3 | Reserved (returns 0) |
| 019 | RMS Volts B 2 | Xfmr Ratio Amps B 1 | Reserved (returns 0) | DIO\#4 Status Output Point 4 |  |
| 020 | RMS Volts C 2 | Xfmr Ratio Amps B 1 | Reserved (returns 0) | DIO\#5 Status Output Point 1 |  |
| 021 | RMS Volts N 2 | Xfmr Ratio Amps C 1 | Virtual Status Input 1 | DIO\#5 Status Output Point 2 |  |
| 022 | RMS Volts AB 2 | Xfmr Ratio Amps C 1 | Virtual Status Input 2 | DIO\#5 Status Output Point 3 |  |
| 023 | RMS Volts BC 2 | Xfmr Ratio Amps N 1 | Virtual Status Input 3 | DIO\#5 Status Output Point 4 |  |
| 024 | RMS Volts CA 2 | Xfmr Ratio Amps N 1 | Virtual Status Input 4 | DIO\#6 Status Output Point 1 |  |
| 025 | RMS Amps A 2 | Xfmr Ratio Volts A 2 | Virtual Status Input 5 | DIO\#6 Status Output Point 2 |  |
| 026 | RMS Amps B 2 | Xfmr Ratio Volts A 2 | Virtual Status Input 6 | DIO\#6 Status Output Point 3 |  |
| 027 | RMS Amps C 2 | Xfmr Ratio Volts B 2 | Virtual Status Input 7 | DIO\#6 Status Output Point 4 |  |
| 028 | Reserved for RMS Amps N 2 | Xfmr Ratio Volts B 2 | Virtual Status Input 8 | Reserved (returns 0) |  |
| 029 | RMS Watts A 1 | Xfmr Ratio Volts C 2 | Virtual Status Input 9 | Reserved (returns 0) |  |
| 030 | RMS Watts B 1 | Xfmr Ratio Volts C 2 | Virtual Status Input 10 | Reserved (returns 0) |  |
| 031 | RMS Watts C 1 | Xfmr Ratio Volts N 2 | Virtual Status Input 11 | Reserved (returns 0) |  |
| 032 | RMS Watts T 1 | Xfmr Ratio Volts N 2 | Virtual Status Input 12 | Reserved (returns 0) |  |
| 033 | RMS VARs A 1 | Xfmr Ratio Amps A 2 | Virtual Status Input 13 | Reserved (returns 0) |  |
| 034 | RMS VARs B 1 | Xfmr Ratio Amps A 2 | Virtual Status Input 14 | Reserved (returns 0) |  |
| 035 | RMS VARs C 1 | Xfmr Ratio Amps B 2 | Virtual Status Input 15 | Reserved (returns 0) |  |
| 036 | RMS VARs T 1 | Xfmr Ratio Amps B 2 | Virtual Status Input 16 | Reserved (returns 0) |  |
| 037 | RMS VAs A 1 | Xfmr Ratio Amps C 2 | Virtual Status Input 17 | Reserved (returns 0) |  |
| 038 | RMS VAs B 1 | Xfmr Ratio Amps C 2 | Virtual Status Input 18 | Reserved (returns 0) |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 039 | RMS VAs C 1 | Reserved for Xfmr Ratio Amps N 2 | Virtual Status Input 19 | Reserved (returns 0) |  |
| 040 | RMS VAs T 1 | Reserved for Xfmr Ratio Amps N 2 | Virtual Status Input 20 | Reserved (returns 0) |  |
| 041 | Power Factor A 1 | Xfmr Ratio Volts R 1 | Virtual Status Input 21 | Reset Energy |  |
| 042 | Power Factor B 1 | Xfmr Ratio Volts R 1 | Virtual Status Input 22 | Reset Demand Amps |  |
| 043 | Power Factor C 1 | Xfmr Ratio Volts R 2 | Virtual Status Input 23 | Reset Demand Volts |  |
| 044 | Power Factor T 1 | Xfmr Ratio Volts R 2 | Virtual Status Input 24 | Reset Demand Power |  |
| 045 | RMS Watts A 2 | Reserved (returns 0) | Virtual Status Input 25 | Reset Demand Harmonic |  |
| 046 | RMS Watts B 2 | Reserved (returns 0) | Virtual Status Input 26 | Reset Received [UCA] GOOSE Parameters <br> [NOTE: This is now referred to as GSSE] |  |
| 047 | RMS Watts C 2 | Reserved (returns 0) | Virtual Status Input 27 | WR1 Recorder Started |  |
| 048 | RMS Watts T 2 | Reserved (returns 0) | Virtual Status Input 28 | WR2 Recorder Started |  |
| 049 | RMS VARs A 2 | Reserved (returns 0) | Virtual Status Input 29 | DR1 Recorder Started |  |
| 050 | RMS VARs B 2 | Reserved (returns 0) | Virtual Status Input 30 | DR2 Recorder Started |  |
| 051 | RMS VARs C 2 | Reserved (returns 0) | Virtual Status Input 31 | Any Recorder Started |  |
| 052 | RMS VARs T 2 | Reserved (returns 0) | Virtual Status Input 32 | WR1 Recorder Completed |  |
| 053 | Meter Type | Reserved (returns 0) | Reserved (returns 0) | WR2 Recorder Completed |  |
| 054 | Reserved (returns 0) | Reserved (returns 0) | Reserved (returns 0) | DR1 Recorder Completed |  |
| 055 | Meter Type | Reserved (returns 0) | Reserved (returns 0) | Disturbance 2 Recorder Completed |  |
| 056 | RMS VAs A 2 | Reserved (returns 0) | Reserved (returns 0) | Any Recorder Completed |  |
| 057 | RMS VAs B 2 | Reserved (returns 0) | Reserved (returns 0) | Trigger WR1 Recorder |  |
| 058 | RMS VAs C 2 | Reserved (returns 0) | Reserved (returns 0) | Trigger WR2 Recorder |  |
| 059 | RMS VAs T 2 | Reserved (returns 0) | Reserved (returns 0) | Trigger DR1 Recorder |  |
| 060 | Power Factor A 2 | Reserved (returns 0) | Reserved (returns 0) | Trigger DR2 Recorder |  |
| 061 | Power Factor B 2 | Config Register 1 | Reserved (returns 0) | Reserved (returns 0) |  |
| 062 | Power Factor C 2 | Config Register 2 | Reserved (returns 0) | Reserved (returns 0) |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 063 | Power Factor T 2 | Tag Register | Reserved (returns 0) | Reserved (returns 0) |  |
| 064 | System Frequency |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 065 | Reserved for System Frequency 2 |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 066 | Reserved (returns 0) |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 067 | Reserved (returns 0) |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 068 | Reserved (returns 0) |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 069 | Reserved (returns 0) |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 070 | Reserved (returns 0) |  | Reserved (returns 0) | Reserved (returns 0) |  |
| 071 | Reserved (returns 0) |  |  | Reserved (returns 0) |  |
| 072 | Reserved (returns 0) |  |  | Reserved (returns 0) |  |
| 073 | Reserved (returns 0) |  |  | Reserved (returns 0) |  |
| 074 | Reserved (returns 0) |  |  | Reserved (returns 0) |  |
| 075 | Reserved (returns 0) |  |  | Reserved (returns 0) |  |
| 076 | Reserved (returns 0) |  |  | Reserved (returns 0) |  |
| 077 | Reserved (returns 0) |  |  | Virtual Status Output Point 1 |  |
| 078 | Reserved (returns 0) |  |  | Virtual Status Output Point 2 |  |
| 079 | Reserved (returns 0) |  |  | Virtual Status Output Point 3 |  |
| 080 | Reserved (returns 0) |  |  | Virtual Status Output Point 4 |  |
| 081 | System Frequency |  |  | Virtual Status Output Point 5 |  |
| 082 | Demand RMS Amps A 1 |  |  | Virtual Status Output Point 6 |  |
| 083 | Demand RMS Amps B 1 |  |  | Virtual Status Output Point 7 |  |
| 084 | Demand RMS Amps C 1 |  |  | Virtual Status Output Point 8 |  |
| 085 | Demand RMS Amps N 1 |  |  | Virtual Status Output Point 9 |  |
| 086 | Max Demand RMS Amps A 1 |  |  | Virtual Status Output Point 10 |  |
| 087 | Max Demand RMS Amps B 1 |  |  | Virtual Status Output Point 11 |  |
| 088 | Max Demand RMS Amps C 1 |  |  | Virtual Status Output Point 12 |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 089 | Max Demand RMS Amps N 1 |  |  | Virtual Status Output Point 13 |  |
| 090 | Demand RMS Volts A 1 |  |  | Virtual Status Output Point 14 |  |
| 091 | Demand RMS Volts B 1 |  |  | Virtual Status Output Point 15 |  |
| 092 | Demand RMS Volts C 1 |  |  | Virtual Status Output Point 16 |  |
| 093 | Demand RMS Volts N 1 |  |  | Virtual Status Output Point 17 |  |
| 094 | Max Demand RMS Volts A 1 |  |  | Virtual Status Output Point 18 |  |
| 095 | Max Demand RMS Volts B 1 |  |  | Virtual Status Output Point 19 |  |
| 096 | Max Demand RMS Volts C 1 |  |  | Virtual Status Output Point 20 |  |
| 097 | Max Demand RMS Volts N 1 |  |  | Virtual Status Output Point 21 |  |
| 098 | Min Demand RMS Volts A 1 |  |  | Virtual Status Output Point 22 |  |
| 099 | Min Demand RMS Volts B 1 |  |  | Virtual Status Output Point 23 |  |
| 100 | Min Demand RMS Volts C 1 |  |  | Virtual Status Output Point 24 |  |
| 101 | Min Demand RMS Volts N 1 |  |  | Virtual Status Output Point 25 |  |
| 102 | Demand RMS Volts AB 1 |  |  | Virtual Status Output Point 26 |  |
| 103 | Demand RMS Volts BC 1 |  |  | Virtual Status Output Point 27 |  |
| 104 | Demand RMS Volts CA 1 |  |  | Virtual Status Output Point 28 |  |
| 105 | Max Demand RMS Volts AB 1 |  |  | Virtual Status Output Point 29 |  |
| 106 | Max Demand RMS Volts BC 1 |  |  | Virtual Status Output Point 30 |  |
| 107 | Max Demand RMS Volts CA 1 |  |  | Virtual Status Output Point 31 |  |
| 108 | Min Demand RMS Volts AB 1 |  |  | Virtual Status Output Point 32 |  |
| 109 | Min Demand RMS Volts BC 1 |  |  | Reserved (returns 0) |  |
| 110 | Min Demand RMS Volts CA 1 |  |  | Reserved (returns 0) |  |
| 111 | Demand RMS Watts Total 1 |  |  | Reserved (returns 0) |  |
| 112 | Demand RMS VARs Total 1 |  |  | Reserved (returns 0) |  |
| 113 | Demand RMS VAs Total 1 |  |  | Reserved (returns 0) |  |
| 114 | Max Demand RMS Watts Total 1 |  |  | Reserved (returns 0) |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 115 | Max Demand RMS VARs Total 1 |  |  | Reserved (returns 0) |  |
| 116 | Max Demand RMS VAs Total 1 |  |  | Reserved (returns 0) |  |
| 117 | Min Demand RMS Watts Total 1 |  |  | Reserved (returns 0) |  |
| 118 | Min Demand RMS VARs Total 1 |  |  | Reserved (returns 0) |  |
| 119 | Min Demand RMS VAs Total 1 |  |  | Reserved (returns 0) |  |
| 120 | Reserved for System Frequency 2 |  |  | Reserved (returns 0) |  |
| 121 | Demand RMS Amps A 2 |  |  | WR1 Recorder Memory Low |  |
| 122 | Demand RMS Amps B 2 |  |  | WR2 Recorder Memory Low |  |
| 123 | Demand RMS Amps C 2 |  |  | DR1 Recorder Memory Low |  |
| 124 | Reserved for Demand RMS Amps N 2 |  |  | DR2 Recorder Memory Low |  |
| 125 | Max Demand RMS Amps A 2 |  |  | Any Recorder Memory Low |  |
| 126 | Max Demand RMS Amps B 2 |  |  | WR1 Recorder Active |  |
| 127 | Max Demand RMS Amps C 2 |  |  | WR2 Recorder Active |  |
| 128 | Reserved for Max Demand RMS Amps N 2 |  |  | DR1 Recorder Active |  |
| 129 | Demand RMS Volts A 2 |  |  | DR2 Recorder Active |  |
| 130 | Demand RMS Volts B 2 |  |  | Any Recorder Active |  |
| 131 | Demand RMS Volts C 2 |  |  | Reserved (returns 0) |  |
| 132 | Demand RMS Volts N 2 |  |  | Reserved (returns 0) |  |
| 133 | Max Demand RMS Volts A 2 |  |  | Reserved (returns 0) |  |
| 134 | Max Demand RMS Volts B 2 |  |  | Reserved (returns 0) |  |
| 135 | Max Demand RMS Volts C 2 |  |  | Reserved (returns 0) |  |
| 136 | Max Demand RMS Volts N 2 |  |  | Reserved (returns 0) |  |
| 137 | Min Demand RMS Volts A 2 |  |  | Reserved (returns 0) |  |
| 138 | Min Demand RMS Volts B 2 |  |  | Reserved (returns 0) |  |
| 139 | Min Demand RMS Volts C 2 |  |  | Reserved (returns 0) |  |
| 140 | Min Demand RMS Volts N 2 |  |  | Reserved (returns 0) |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 141 | Demand RMS Volts AB 2 |  |  |  |  |
| 142 | Demand RMS Volts BC 2 |  |  |  |  |
| 143 | Demand RMS Volts CA 2 |  |  |  |  |
| 144 | Max Demand RMS Volts AB 2 |  |  |  |  |
| 145 | Max Demand RMS Volts BC 2 |  |  |  |  |
| 146 | Max Demand RMS Volts CA 2 |  |  |  |  |
| 147 | Min Demand RMS Volts AB 2 |  |  |  |  |
| 148 | Min Demand RMS Volts BC 2 |  |  |  |  |
| 149 | Min Demand RMS Volts CA 2 |  |  |  |  |
| 150 | Demand RMS Watts Total 2 |  |  |  |  |
| 151 | Demand RMS VARs Total 2 |  |  |  |  |
| 152 | Demand RMS VAs Total 2 |  |  |  |  |
| 153 | Max Demand RMS Watts Total 2 |  |  |  |  |
| 154 | Max Demand RMS VARs Total 2 |  |  |  |  |
| 155 | Max Demand RMS VAs Total 2 |  |  |  |  |
| 156 | Min Demand RMS Watts Total 2 |  |  |  |  |
| 157 | Min Demand RMS VARs Total 2 |  |  |  |  |
| 158 | Min Demand RMS VAs Total 2 |  |  |  |  |
| 159 | Reserved (returns 0) |  |  |  |  |
| 160 | Reserved (returns 0) |  |  |  |  |
| 161 | Reserved (returns 0) |  |  |  |  |
| 162 | Reserved (returns 0) |  |  |  |  |
| 163 | Reserved (returns 0) |  |  |  |  |
| 164 | Reserved (returns 0) |  |  |  |  |
| 165 | Reserved (returns 0) |  |  |  |  |
| 166 | Reserved (returns 0) |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 167 | Reserved (returns 0) |  |  |  |  |
| 168 | Reserved (returns 0) |  |  |  |  |
| 169 | Reserved (returns 0) |  |  |  |  |
| 170 | Reserved (returns 0) |  |  |  |  |
| 171 | Phase Angle Volts A 1-2 |  |  |  |  |
| 172 | Phase Angle Volts B 1-2 |  |  |  |  |
| 173 | Phase Angle Volts C 1-2 |  |  |  |  |
| 174 | Phase Angle Volts A 1-R 1 |  |  |  |  |
| 175 | Phase Angle Volts B 1-R 1 |  |  |  |  |
| 176 | Phase Angle Volts C 1-R 1 |  |  |  |  |
| 177 | Phase Angle Volts A 1-R 2 |  |  |  |  |
| 178 | Phase Angle Volts B 1-R 2 |  |  |  |  |
| 179 | Phase Angle Volts C 1-R 2 |  |  |  |  |
| 180 | RMS Volts A 1 |  |  |  |  |
| 181 | RMS Volts B 1 |  |  |  |  |
| 182 | RMS Volts C 1 |  |  |  |  |
| 183 | RMS Volts N 1 |  |  |  |  |
| 184 | RMS Volts R 1 |  |  |  |  |
| 185 | RMS Volts R 2 |  |  |  |  |
| 186 | Frequency Volts A 1 |  |  |  |  |
| 187 | Frequency Volts B 1 |  |  |  |  |
| 188 | Frequency Volts C 1 |  |  |  |  |
| 189 | Frequency R 1 |  |  |  |  |
| 190 | Frequency R 2 |  |  |  |  |
| 191 | RMS Volts A 2 |  |  |  |  |
| 192 | RMS Volts B 2 |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 193 | RMS Volts C 2 |  |  |  |  |
| 194 | RMS Volts N 2 |  |  |  |  |
| 195 | Frequency Volts A 2 |  |  |  |  |
| 196 | Frequency Volts B 2 |  |  |  |  |
| 197 | Frequency Volts C 2 |  |  |  |  |
| 198 | Reserved (returns 0) |  |  |  |  |
| 199 | Reserved (returns 0) |  |  |  |  |
| 200 | Reserved (returns 0) |  |  |  |  |
| 201 | Reserved (returns 0) |  |  |  |  |
| 202 | Reserved (returns 0) |  |  |  |  |
| 203 | Reserved (returns 0) |  |  |  |  |
| 204 | Reserved (returns 0) |  |  |  |  |
| 205 | Reserved (returns 0) |  |  |  |  |
| 206 | Reserved (returns 0) |  |  |  |  |
| 207 | Reserved (returns 0) |  |  |  |  |
| 208 | Reserved (returns 0) |  |  |  |  |
| 209 | Reserved (returns 0 ) |  |  |  |  |
| 210 | Reserved (returns 0) |  |  |  |  |
| 211 | DSP Version |  |  |  |  |
| 212 | Protocol Version |  |  |  |  |
| 213 | Time Sync Error (msec) |  |  |  |  |
| 214 | IrigB Time Sync (0 or 1) |  |  |  |  |
| 215 | (UCA) Network Time (0 or 1)) |  |  |  |  |
| 216 | SNTP Time Sync (0 or 1) |  |  |  |  |
| 217 | DNP Time Sync (0 or 1) |  |  |  |  |
| 218 | Reserved (returns 0) |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 219 | Best Clock Source (0 thru 5) |  |  |  |  |
| 220 | Reserved (returns 0) |  |  |  |  |
| 221 | Reserved (returns 0 ) |  |  |  |  |
| 222 | Reserved (returns 0) |  |  |  |  |
| 223 | Reserved (returns 0) |  |  |  |  |
| 224 | Reserved (returns 0) |  |  |  |  |
| 225 | Reserved (returns 0) |  |  |  |  |
| 226 | Reserved (returns 0) |  |  |  |  |
| 227 | Reserved (returns 0) |  |  |  |  |
| 228 | Reserved (returns 0) |  |  |  |  |
| 229 | Reserved (returns 0) |  |  |  |  |
| 230 | Reserved (returns 0) |  |  |  |  |
| 231 | DIO\#O Input |  |  |  |  |
| 232 | DIO\#1 Input |  |  |  |  |
| 233 | DIO\#2 Input |  |  |  |  |
| 234 | DIO\#3 Input |  |  |  |  |
| 235 | DIO\#4 Input |  |  |  |  |
| 236 | DIO\#5 Input |  |  |  |  |
| 237 | DIO\#6 Input |  |  |  |  |
| 238 | Reserved (returns 0) |  |  |  |  |
| 239 | Reserved (returns 0) |  |  |  |  |
| 240 | Reserved (returns 0) |  |  |  |  |
| 241 | Reserved (returns 0) |  |  |  |  |
| 242 | Reserved (returns 0) |  |  |  |  |
| 243 | Reserved (returns 0) |  |  |  |  |
| 244 | Reserved (returns 0) |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 245 | Reserved (returns 0) |  |  |  |  |
| 246 | Reserved (returns 0) |  |  |  |  |
| 247 | Reserved (returns 0) |  |  |  |  |
| 248 | Reserved (returns 0) |  |  |  |  |
| 249 | Reserved (returns 0) |  |  |  |  |
| 250 | Reserved (returns 0) |  |  |  |  |
| 251 | RMS Volts A 1 |  |  |  |  |
| 252 | RMS Volts B 1 |  |  |  |  |
| 253 | RMS Volts C 1 |  |  |  |  |
| 254 | Phase Angle RMS Volts A 1 Harmonic 01 |  |  |  |  |
| 255 | Phase Angle RMS Volts B 1 Harmonic 01 |  |  |  |  |
| 256 | Phase Angle RMS Volts C 1 Harmonic 01 |  |  |  |  |
| 257 | RMS Amps A 1 |  |  |  |  |
| 258 | RMS Amps B 1 |  |  |  |  |
| 259 | RMS Amps C 1 |  |  |  |  |
| 260 | Phase Angle RMS Amps A 1 Harmonic 01 |  |  |  |  |
| 261 | Phase Angle RMS Amps B 1 Harmonic 01 |  |  |  |  |
| 262 | Phase Angle RMS Amps C 1 Harmonic 01 |  |  |  |  |
| 263 | RMS Volts A 2 |  |  |  |  |
| 264 | RMS Volts B 2 |  |  |  |  |
| 265 | RMS Volts C 2 |  |  |  |  |
| 266 | Phase Angle RMS Volts A 2 Harmonic 01 |  |  |  |  |
| 267 | Phase Angle RMS Volts B 2 Harmonic 01 |  |  |  |  |
| 268 | Phase Angle RMS Volts C 2 Harmonic 01 |  |  |  |  |
| 269 | RMS Amps A 2 |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 270 | RMS Amps B 2 |  |  |  |  |
| 271 | RMS Amps C 2 |  |  |  |  |
| 272 | Phase Angle RMS Amps A 2 Harmonic 01 |  |  |  |  |
| 273 | Phase Angle RMS Amps B 2 Harmonic 01 |  |  |  |  |
| 274 | Phase Angle RMS Amps C 2 Harmonic 01 |  |  |  |  |
| 275 | Reserved (returns 0) |  |  |  |  |
| 276 | Reserved (returns 0) |  |  |  |  |
| 277 | Reserved (returns 0) |  |  |  |  |
| 278 | Reserved (returns 0) |  |  |  |  |
| 279 | Reserved (returns 0) |  |  |  |  |
| 280 | Reserved (returns 0) |  |  |  |  |
| 281 | Reserved (returns 0) |  |  |  |  |
| 282 | Reserved (returns 0) |  |  |  |  |
| 283 | Reserved (returns 0) |  |  |  |  |
| 284 | Reserved (returns 0 ) |  |  |  |  |
| 285 | Reserved (returns 0) |  |  |  |  |
| 286 | Reserved (returns 0) |  |  |  |  |
| 287 | Reserved (returns 0) |  |  |  |  |
| 288 | Reserved (returns 0) |  |  |  |  |
| 289 | Reserved (returns 0) |  |  |  |  |
| 290 | Reserved (returns 0) |  |  |  |  |
| 291 | Impedance A 1 |  |  |  |  |
| 292 | Impedance B 1 |  |  |  |  |
| 293 | Impedance C 1 |  |  |  |  |
| 294 | Resistance A 1 |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 295 | Resistance B 1 |  |  |  |  |
| 296 | Resistance C 1 |  |  |  |  |
| 297 | Reactance A 1 |  |  |  |  |
| 298 | Reactance B 1 |  |  |  |  |
| 299 | Reactance C 1 |  |  |  |  |
| 300 | Phase Angle A 1 |  |  |  |  |
| 301 | Phase Angle B 1 |  |  |  |  |
| 302 | Phase Angle C 1 |  |  |  |  |
| 303 | Impedance A 2 |  |  |  |  |
| 304 | Impedance B 2 |  |  |  |  |
| 305 | Impedance C 2 |  |  |  |  |
| 306 | Resistance A 2 |  |  |  |  |
| 307 | Resistance B 2 |  |  |  |  |
| 308 | Resistance C 2 |  |  |  |  |
| 309 | Reactance A 2 |  |  |  |  |
| 310 | Reactance B 2 |  |  |  |  |
| 311 | Reactance C 2 |  |  |  |  |
| 312 | Phase Angle A 2 |  |  |  |  |
| 313 | Phase Angle B 2 |  |  |  |  |
| 314 | Phase Angle C 2 |  |  |  |  |
| 315 | Reserved (returns 0) |  |  |  |  |
| 316 | Reserved (returns 0) |  |  |  |  |
| 317 | Reserved (returns 0) |  |  |  |  |
| 318 | Reserved (returns 0) |  |  |  |  |
| 319 | Reserved (returns 0) |  |  |  |  |
| 320 | Reserved (returns 0) |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 321 | Reserved (returns 0) |  |  |  |  |
| 322 | Reserved (returns 0) |  |  |  |  |
| 323 | Reserved (returns 0) |  |  |  |  |
| 324 | Reserved (returns 0) |  |  |  |  |
| 325 | Reserved (returns 0) |  |  |  |  |
| 326 | Reserved (returns 0) |  |  |  |  |
| 327 | Reserved (returns 0) |  |  |  |  |
| 328 | Reserved (returns 0) |  |  |  |  |
| 329 | Reserved (returns 0) |  |  |  |  |
| 330 | Reserved (returns 0) |  |  |  |  |
| 331 | TI\#1 Input 1 |  |  |  |  |
| 332 | TI\#1 Input 2 |  |  |  |  |
| 333 | TI\#1 Input 3 |  |  |  |  |
| 334 | TI\#1 Input 4 |  |  |  |  |
| 335 | TI\#1 Input 5 |  |  |  |  |
| 336 | TI\#1 Input 6 |  |  |  |  |
| 337 | TI\#1 Input 7 |  |  |  |  |
| 338 | TI\#1 Input 8 |  |  |  |  |
| 339 | TI\#2 Input 1 |  |  |  |  |
| 340 | T1\#2 Input 2 |  |  |  |  |
| 341 | TI\#2 Input 3 |  |  |  |  |
| 342 | T1\#2 Input 4 |  |  |  |  |
| 343 | TI\#2 Input 5 |  |  |  |  |
| 344 | T1\#2 Input 6 |  |  |  |  |
| 345 | T1\#2 Input 7 |  |  |  |  |
| 346 | TI\#2 Input 8 |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 347 | TI\#3 Input 1 |  |  |  |  |
| 348 | TI\#3 Input 2 |  |  |  |  |
| 349 | TI\#3 Input 3 |  |  |  |  |
| 350 | TI\#3 Input 4 |  |  |  |  |
| 351 | TI\#3 Input 5 |  |  |  |  |
| 352 | TI\#3 Input 6 |  |  |  |  |
| 353 | TI\#3 Input 7 |  |  |  |  |
| 354 | TI\#3 Input 8 |  |  |  |  |
| 355 | TI\#4 Input 1 |  |  |  |  |
| 356 | TI\#4 Input 2 |  |  |  |  |
| 357 | TI\#4 Input 3 |  |  |  |  |
| 358 | TI\#4 Input 4 |  |  |  |  |
| 359 | TI\#4 Input 5 |  |  |  |  |
| 360 | TI\#4 Input 6 |  |  |  |  |
| 361 | TI\#4 Input 7 |  |  |  |  |
| 362 | TI\#4 Input 8 |  |  |  |  |
| 363 | TI\#5 Input 1 |  |  |  |  |
| 364 | TI\#\# Input 2 |  |  |  |  |
| 365 | TI\#5 Input 3 |  |  |  |  |
| 366 | TI\#\# Input 4 |  |  |  |  |
| 367 | TI\# 5 Input 5 |  |  |  |  |
| 368 | TI\# 5 Input 6 |  |  |  |  |
| 369 | TI\#5 Input 7 |  |  |  |  |
| 370 | TI\#5 Input 8 |  |  |  |  |
| 371 | TI\#6 Input 1 |  |  |  |  |
| 372 | TI\#6 Input 2 |  |  |  |  |


|  | BAF (BiTRONICS Advanced Fixed) <br> Analog Inputs | Analog Outputs | Binary Inputs | Binary Outputs | Counters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 373 | TI\#6 Input 3 |  |  |  |  |
| 374 | TI\#6 Input 4 |  |  |  |  |
| 375 | TI\#6 Input 5 |  |  |  |  |
| 376 | TI\#6 Input 6 |  |  |  |  |
| 377 | TI\#6 Input 7 |  |  |  |  |
| 378 | TI\#6 Input 8 |  |  |  |  |
| 379 | TI\#7 Input 1 |  |  |  |  |
| 380 | TI\#7 Input 2 |  |  |  |  |
| 381 | TI\#7 Input 3 |  |  |  |  |
| 382 | TI\#7 Input 4 |  |  |  |  |
| 383 | Tı\#7 Input 5 |  |  |  |  |
| 384 | Tı\#7 Input 6 |  |  |  |  |
| 385 | Tı\#7 Input 7 |  |  |  |  |
| 386 | TI\#7 Input 8 |  |  |  |  |

Please note that the HAF (Harmonics Advanced Fixed) Point Set is identical to BAF through point 386, but adds harmonics and phase angles for L-L and L-N voltages and for currents for all 63 harmonics through point 3271

| DNP V3．0 <br> DEVICE PROFILE DOCUMENT |  |
| :---: | :---: |
|  |  |
| Vendor Name：Alstom Grid |  |
| Device Name：Mx7x |  |
| Highest DNP Level Supported： <br> For Requests： Level 2 <br> For Responses： Level 2 | Device Function： <br> Master <br> 区 Slave |
| Notable objects，functions，and／or qualifiers supported in addition to the Highest DNP Levels Supported： <br> For static（non－change－event）object requests，request qualifier codes 00 and 01 （start－stop）， 07 and 08 （limited quantity），and 17 and 28 （index）are supported in addition to request qualifier code 06 （no range）．Static object requests sent with qualifiers $00,01,06,07$ ，or 08 ，will be responded with qualifiers 00 or 01 ．Static object requests sent with qualifiers 17 or 28 will be responded with qualifiers 17 or 28 ．For change－event object requests，qualifiers 17 or 28 are always responded． <br> 16－bit Analog Change Events may be requested． <br> The read function code for Object 50 （Time and Date），variation 1，is supported． <br> Sequential file transfer，Object 70，variations 3 through 7，are supported． |  |
| Maximum Data Link Frame Size（octets）： <br> Transmitted： 292 <br> Received 292 | Maximum Application Fragment Size（octets）： <br> Transmitted： 2048 <br> Received 2048 |
| Maximum Data Link Re－tries： None Fixed <br> 区 Configurable from 0 to 255 | Maximum Application Layer Re－tries： <br> 区 None <br> $\square \quad$ Configurable |
| Requires Data Link Layer Confirmation： Never Always Sometimes <br> ® Configurable as：Never，Only for multi－frame messages，or Always |  |
| Requires Application Layer Confirmation： |  |
| $\square \quad$ Never |  |
| $\square \quad$ Always |  |
| $\square \quad$ When reporting Event Data（Slave devices only） |  |
| $\square \quad$ When sending multi－fragment responses（Slave devices only） |  |
| $\square \quad$ Sometimes |  |
| ■ Configurable as：＂Only when re or multi－fragment messages．＂ | vent data＂，or＂When reporting event data |

## DNP V3．0

## DEVICE PROFILE DOCUMENT

Timeouts while waiting for：

| Data Link Confirm： |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| $\square \quad$ None $\square \quad$ Fixed at | $\square$ | Variable | 区 | Configurable． |
| Complete Appl．Fragment： |  |  |  |  |
| 区 None $\square$ Fixed at | $\square$ | Variable | $\square$ | Configurable |
| Application Confirm： |  |  |  |  |
| $\square \quad$ None $\square \quad$ Fixed at | $\square$ | Variable | 区 | Configurable． |
| Complete Appl．Response： |  |  |  |  |
| 区 None $\square$ Fixed at | $\square$ | Variable | $\square$ | Configurable |

Others：Transmission Delay，configurable．
Arm Select Timeout，configurable．
Application File Timeout，configurable．


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[^0]:    * H10/H11

